

EAST Search History

S10 8	1	((correlat\$4 synchroni\$8) with (interrupt\$4) and (375/130,140,141,143,147,152,150,142,354,316,259,295,357,356,355.ccls. 370/310,342.ccls.) and \$2cdma and IPDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 14:25
S11 9	26	((correlat\$4 synchroni\$8) with (interrupt\$4)) and ((lmu measur\$7) with (interrupt\$6)) and (375/130,140,141,143,147,152,150,142,354,316,259,295,357,356,355.ccls. 370/310,342.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 14:27
S1	1	10/661376	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:28
S12 1	0	(resum4 near4 synchroni\$8) same (interrupt\$4 (power near3 off)) and (phase near4 shift\$4) same ((timing time) near4 (elapsed difference))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:31
S12 0	6	synchroni\$8 same (interrupt\$4 (power near3 off)) same (phase near4 shift\$4) same ((timing time) near4 (elapsed difference))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:31
S12 3	8	S122 not S120	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:32
S12 2	14	(synchroni\$8) and (interrupt\$4 (power near3 off)) same (phase near4 shift\$4) same ((timing time) near4 (elapsed difference))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:32
S12 5	38	S124 not S122	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:41
S12 4	44	(synchroni\$8) with (interrupt\$4 (power near3 off)) and (phase near4 shift\$4) same ((timing time) near4 (elapsed difference))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/28 09:41

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L1	820	(mov\$4 shift\$4) near4 (correlat\$4 phase) with interrupt\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:08
L2	62	(mov\$4 shift\$4) near4 (correlat\$4 phase) with interrupt\$4 same (synchroniz\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:09
L5	2	(mov\$4 shift\$4) near4 (correlat\$4) with interrupt\$4 same (synchroniz\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:10
L4	0	(mov\$4 shift\$4) near4 (correlat\$4) with interrupt\$4 same (synchroniz\$7) and ("375".clas. "455".clas. "370".clas.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:10
L3	9	(mov\$4 shift\$4) near4 (correlat\$4 phase) with interrupt\$4 same (synchroniz\$7) and ("375".clas. "455".clas. "370".clas.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:10
S11 0	4	((((correlat\$4 synchroni\$8) with (interrupt\$4)) same (matched near3 filter\$4)) and (375/130,140, 141,143,147,152,150,142,354,316, 259,295,357,356,355.ccls. 370/310, 342.ccls.) and \$2cdma	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:14
L6	5	((((correlat\$4 synchroni\$8) with (interrupt\$4)) same ((slid\$4 near3 window\$4)(phase near4 shift\$4 slid\$4 mov\$4))) and (375/130,140, 141,143,147,152,150,142,354,316, 259,295,357,356,355.ccls. 370/310, 342.ccls.) and \$2cdma	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/06 17:43


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[Preferences](#)[Web](#) [Groups](#) Results **1 - 10** of about **9,750** for **shift phase elapsed time interrupt synchronization**. (0.29 se

Microprocessor controlled phase shifter - Patent 4628460

5 is a flow chart for the line **sync interrupt** routine for the main routine that stores the total running or **elapsed time** between line **sync** occurrences. ...

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Code phase synchronization - Patent 20050094716

If new channel response samples were stored during the IPDL after the **interrupt**, a **phase shift** is added which takes into account the **time** difference between ...

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Real-life timer interval adjustment - US Patent 5412624

In a preferred embodiment, a **shift** in the **interrupt** frequency is ... iii) The **elapsed time** between the start and stop marks which is assumed to be accurate. ...

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Real-life timer interval adjustment - US Patent 5412624

performing the **time**-critical functions in response to the **interrupts**; determining a **shift** in the **interrupt** frequency using timing data from a substantially ...

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signals are delayed according to the **phase-shift** and summed "**Elapsed time** on arrival:

A simple and versatile primitive for **time syn**- ...

www.vuse.vanderbilt.edu/~koutsoxd/www/Publications/kalamataWorkshopVandy.pdf -

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The **elapsed** timer **times** out special events that take place during ... Another feature included here is the so-called Fail-safe **interrupt** ...

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Next, the increment time_adj to advance the kernel **time** variable at each timer **interrupt** is calculated. from the **phase** (time_offset) and frequency ...

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as a separate **interrupt** minor timing variations in the frequency modulation will have

Returns **elapsed time** between two events. GenerateFakePLCMessage ...

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Huygens Glossary

BPSK: Bi **Phase Shift** Keying CAM: Command Approval Meeting RTC: Real **Time** Clock

RTI: Real **Time Interrupt** RTU: Remote Terminal Unit ...